



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 27.2

CELSIUS R550, Intel Xeon X5260, 3.33 GHz

SPECint_base2006 = 22.8

CPU2006 license: 22

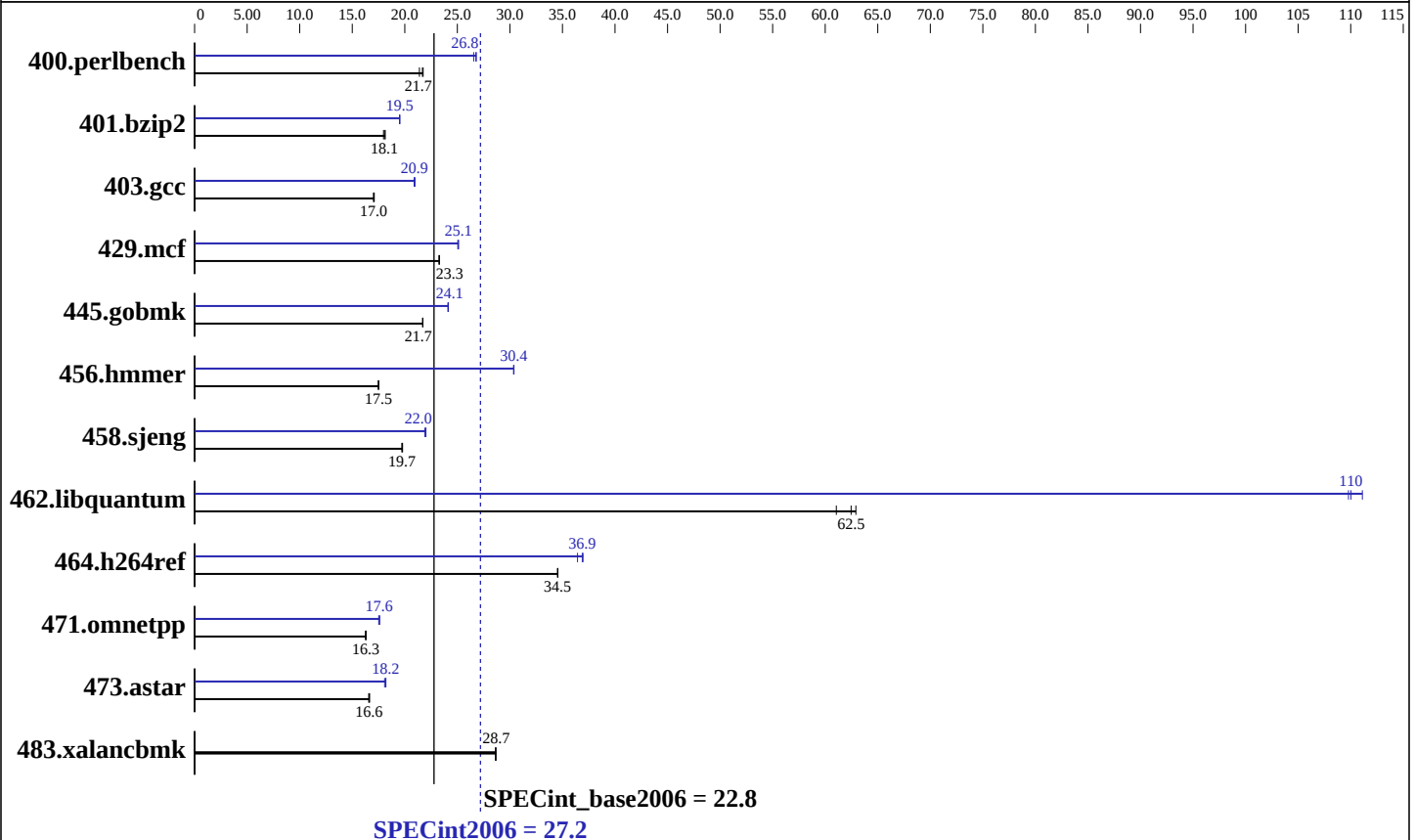
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
CPU Characteristics: 3333
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.1
binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	457	21.4	449	21.7	<u>451</u>	<u>21.7</u>	<u>365</u>	<u>26.8</u>	368	26.6	364	26.8
401.bzip2	532	18.1	<u>534</u>	<u>18.1</u>	537	18.0	495	19.5	<u>494</u>	<u>19.5</u>	494	19.5
403.gcc	471	17.1	473	17.0	<u>472</u>	<u>17.0</u>	385	20.9	<u>385</u>	<u>20.9</u>	384	21.0
429.mcf	391	23.3	392	23.3	<u>392</u>	<u>23.3</u>	364	25.0	363	25.1	<u>363</u>	<u>25.1</u>
445.gobmk	<u>483</u>	<u>21.7</u>	483	21.7	483	21.7	<u>435</u>	<u>24.1</u>	435	24.1	435	24.1
456.hmmer	<u>533</u>	<u>17.5</u>	533	17.5	533	17.5	307	30.4	<u>307</u>	<u>30.4</u>	307	30.4
458.sjeng	<u>613</u>	<u>19.7</u>	611	19.8	613	19.7	552	21.9	<u>551</u>	<u>22.0</u>	550	22.0
462.libquantum	329	62.9	<u>332</u>	<u>62.5</u>	339	61.1	<u>188</u>	<u>110</u>	186	111	189	110
464.h264ref	640	34.6	641	34.5	<u>641</u>	<u>34.5</u>	<u>600</u>	<u>36.9</u>	607	36.4	599	37.0
471.omnetpp	384	16.3	383	16.3	<u>383</u>	<u>16.3</u>	357	17.5	<u>356</u>	<u>17.6</u>	355	17.6
473.astar	422	16.6	424	16.6	<u>422</u>	<u>16.6</u>	<u>386</u>	<u>18.2</u>	386	18.2	388	18.1
483.xalancbmk	241	28.7	<u>241</u>	<u>28.7</u>	241	28.6	<u>241</u>	<u>28.7</u>	<u>241</u>	<u>28.7</u>	241	28.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnoopFilter = Disable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -w1, -z, muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20080124.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20080124.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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