



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 27.1

CELSIUS R650, Intel Xeon X5260, 3.33 GHz

SPECint_base2006 = 22.6

CPU2006 license: 22

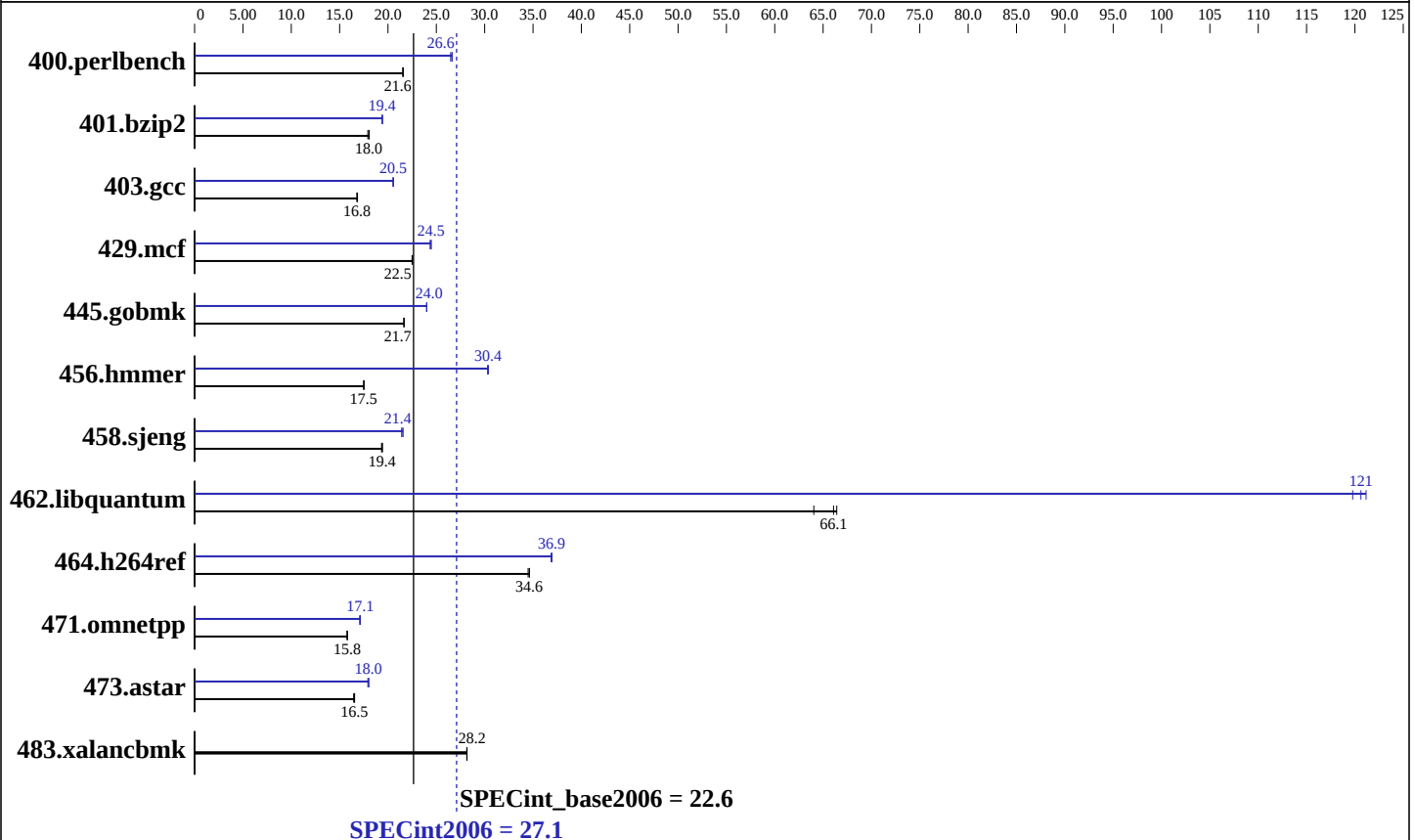
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
CPU Characteristics: 3333
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel: Yes
File System: ext3
System State: Multi-User, Run Level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.1
binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>453</u>	<u>21.6</u>	454	21.5	453	21.6	<u>367</u>	<u>26.6</u>	367	26.6	369	26.5
401.bzip2	534	18.1	<u>535</u>	<u>18.0</u>	539	17.9	497	19.4	497	19.4	<u>497</u>	<u>19.4</u>
403.gcc	<u>479</u>	<u>16.8</u>	479	16.8	478	16.8	392	20.5	<u>392</u>	<u>20.5</u>	391	20.6
429.mcf	406	22.5	<u>405</u>	<u>22.5</u>	405	22.5	374	24.4	373	24.5	<u>373</u>	<u>24.5</u>
445.gobmk	485	21.7	484	21.7	<u>484</u>	<u>21.7</u>	<u>437</u>	<u>24.0</u>	437	24.0	437	24.0
456.hmmer	<u>533</u>	<u>17.5</u>	533	17.5	533	17.5	307	30.4	<u>307</u>	<u>30.4</u>	308	30.3
458.sjeng	<u>624</u>	<u>19.4</u>	623	19.4	626	19.3	<u>564</u>	<u>21.4</u>	565	21.4	561	21.6
462.libquantum	324	64.0	<u>314</u>	<u>66.1</u>	312	66.4	171	121	<u>172</u>	<u>121</u>	173	120
464.h264ref	639	34.6	642	34.5	<u>639</u>	<u>34.6</u>	600	36.9	<u>599</u>	<u>36.9</u>	599	37.0
471.omnetpp	<u>396</u>	<u>15.8</u>	395	15.8	396	15.8	365	17.1	<u>365</u>	<u>17.1</u>	366	17.1
473.astar	424	16.5	427	16.5	<u>425</u>	<u>16.5</u>	392	17.9	390	18.0	<u>390</u>	<u>18.0</u>
483.xalancbmk	245	28.2	245	28.2	<u>245</u>	<u>28.2</u>	245	28.2	245	28.2	<u>245</u>	<u>28.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnoopFilter = Disable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

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<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -wL, -z, muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20080124.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20080124.00.xml>

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