



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 40.4

CELSIUS M460, Intel Core 2 Duo E8400

SPECint_rate_base2006 = 36.7

CPU2006 license: 22

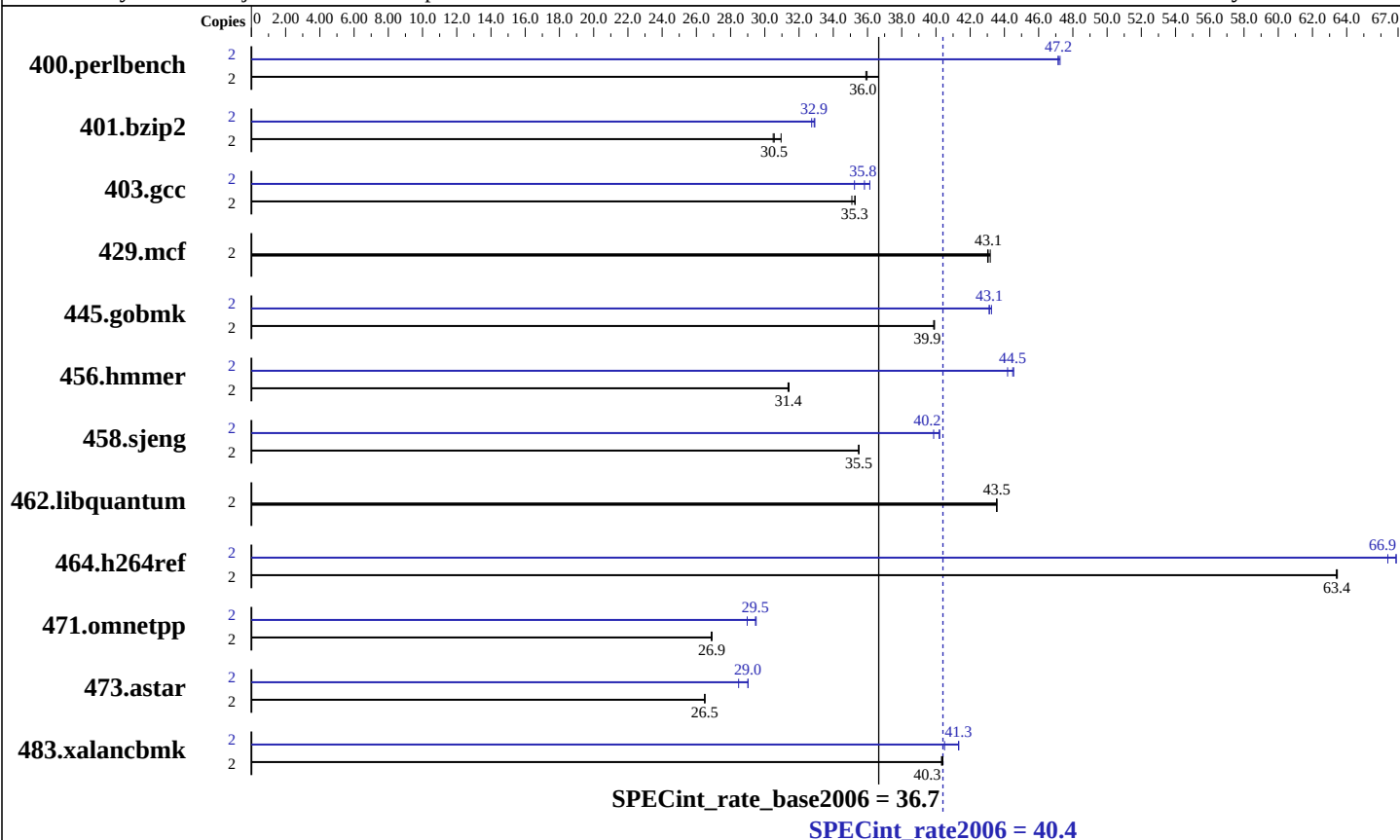
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8400
CPU Characteristics:
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Vista64 Ultimate
Compiler: Intel C++ Compiler
for applications running on IA-32,
Version 10.1, Build 20070913
Intel C++ Compiler
for applications running on Intel 64,
Version 10.1, Build 20070913
Microsoft Visual Studio 2005 with SP1
(for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	533	36.6	544	35.9	543	36.0	2	414	47.2	415	47.1	414	47.3
401.bzip2	2	624	30.9	632	30.5	633	30.5	2	586	32.9	587	32.9	590	32.7
403.gcc	2	456	35.3	457	35.3	459	35.1	2	446	36.1	449	35.8	457	35.2
429.mcf	2	424	43.0	422	43.2	424	43.1	2	424	43.0	422	43.2	424	43.1
445.gobmk	2	526	39.9	526	39.9	526	39.9	2	487	43.1	485	43.2	487	43.1
456.hmmmer	2	595	31.4	594	31.4	595	31.4	2	419	44.5	419	44.5	422	44.2
458.sjeng	2	682	35.5	682	35.5	682	35.5	2	602	40.2	602	40.2	607	39.9
462.libquantum	2	951	43.6	952	43.5	952	43.5	2	951	43.6	952	43.5	952	43.5
464.h264ref	2	698	63.5	698	63.4	698	63.4	2	662	66.9	662	66.9	667	66.4
471.omnetpp	2	465	26.9	465	26.9	465	26.9	2	424	29.5	424	29.5	431	29.0
473.astar	2	530	26.5	530	26.5	530	26.5	2	484	29.0	484	29.0	493	28.5
483.xalancbmk	2	342	40.3	342	40.4	342	40.3	2	334	41.3	334	41.3	341	40.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000

C++ benchmarks:
-fast -Qcxx_features -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99

401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

C++ benchmarks:
icl -Qvc8



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Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmr: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features -F512000000 shlw32M.lib
-link -FORCE:MULTIPLE



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.xml

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For other inquiries, please contact webmaster@spec.org.

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