



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 41.9

CELSIUS M460, Intel Core 2 Duo E8500

SPECint_rate_base2006 = 38.1

CPU2006 license: 22

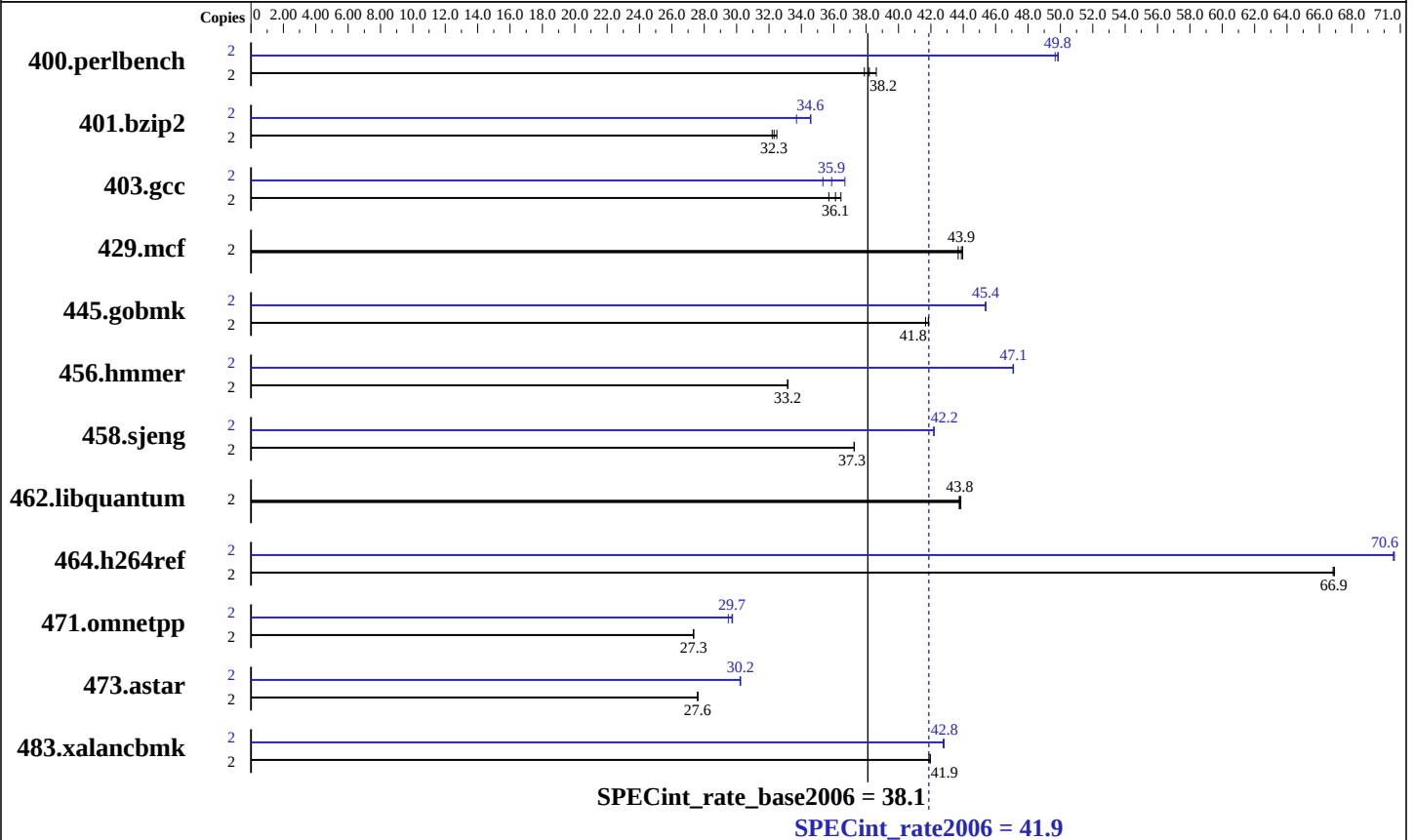
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8500
CPU Characteristics:
CPU MHz: 3167
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Vista64 Ultimate
Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913
Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	506	38.6	516	37.9	<u>512</u>	<u>38.2</u>	2	393	49.7	392	49.9	<u>392</u>	<u>49.8</u>
401.bzip2	2	<u>597</u>	<u>32.3</u>	599	32.2	594	32.5	2	573	33.7	558	34.6	<u>559</u>	<u>34.6</u>
403.gcc	2	442	36.4	<u>446</u>	<u>36.1</u>	451	35.7	2	439	36.7	456	35.3	<u>449</u>	<u>35.9</u>
429.mcf	2	415	44.0	418	43.7	<u>416</u>	<u>43.9</u>	2	415	44.0	418	43.7	<u>416</u>	<u>43.9</u>
445.gobmk	2	501	41.9	<u>501</u>	<u>41.8</u>	503	41.7	2	462	45.4	<u>462</u>	<u>45.4</u>	462	45.4
456.hmmer	2	<u>563</u>	<u>33.2</u>	563	33.1	562	33.2	2	396	47.1	396	47.1	<u>396</u>	<u>47.1</u>
458.sjeng	2	650	37.3	649	37.3	<u>649</u>	<u>37.3</u>	2	574	42.2	574	42.2	<u>574</u>	<u>42.2</u>
462.libquantum	2	947	43.7	945	43.8	<u>946</u>	<u>43.8</u>	2	947	43.7	945	43.8	<u>946</u>	<u>43.8</u>
464.h264ref	2	661	66.9	662	66.8	<u>662</u>	<u>66.9</u>	2	627	70.6	<u>627</u>	<u>70.6</u>	627	70.6
471.omnetpp	2	457	27.3	457	27.3	<u>457</u>	<u>27.3</u>	2	420	29.7	<u>421</u>	<u>29.7</u>	424	29.5
473.astar	2	509	27.6	<u>509</u>	<u>27.6</u>	509	27.6	2	465	30.2	464	30.2	<u>464</u>	<u>30.2</u>
483.xalancbmk	2	329	42.0	<u>329</u>	<u>41.9</u>	330	41.9	2	<u>322</u>	<u>42.8</u>	322	42.8	323	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -Qvec-guard-write -F512000000

C++ benchmarks:

-fast -Qcxx_features -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icl -Qvc8 -Qc99

401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

C++ benchmarks:

icl -Qvc8



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Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmr: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features -F512000000 shlw32M.lib
-link -FORCE:MULTIPLE



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Thu Apr 3 18:48:11 2008 by SPEC CPU2006 PS/PDF formatter v5614.