



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®2006 = 17.6

CELSIUS V840, AMD Opteron 2350 (2.0 GHz)

SPECfp_base2006 = 15.8

CPU2006 license: 22

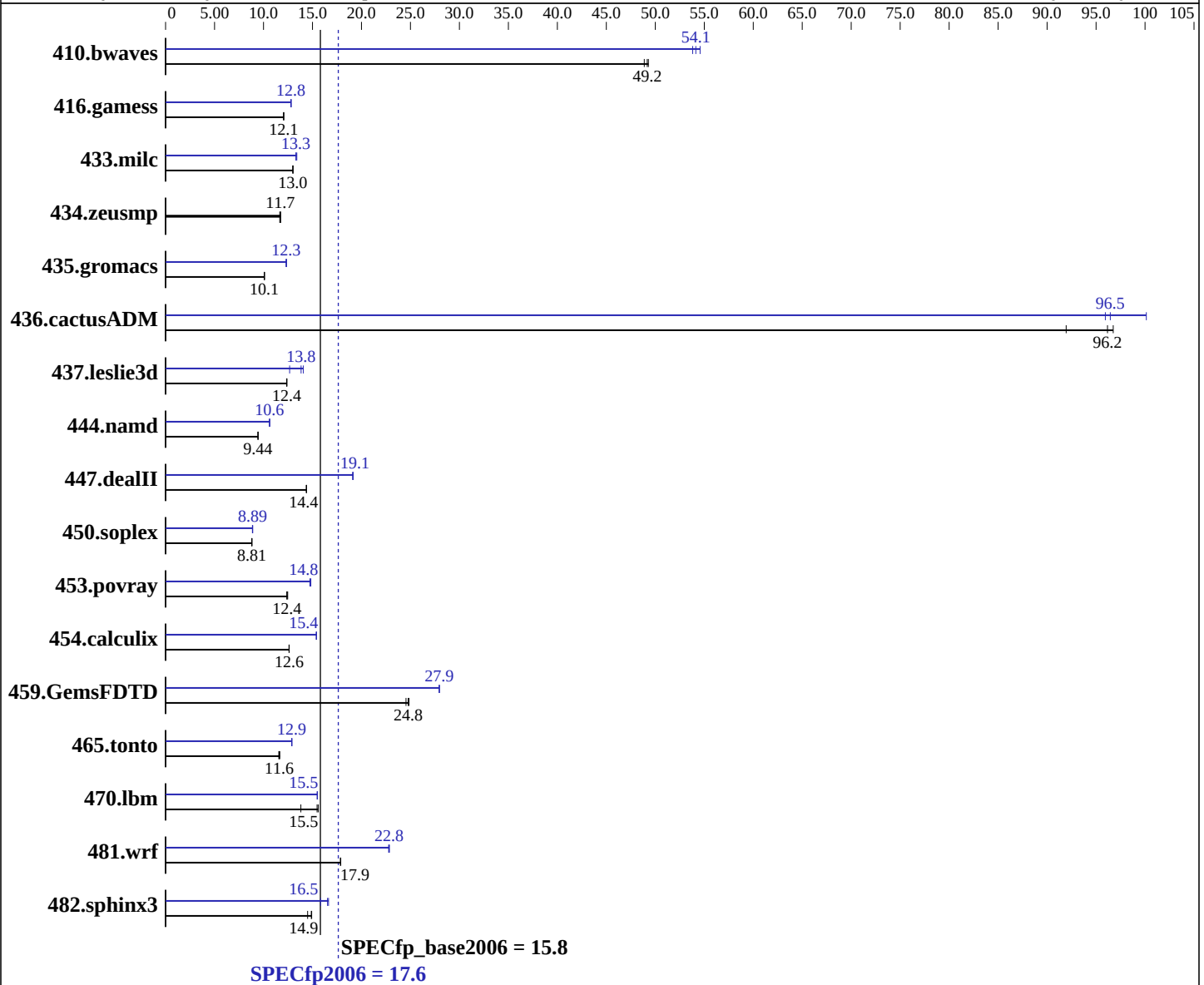
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2008

Hardware Availability: May-2008

Software Availability: May-2008



Hardware

CPU Name: AMD Opteron 2350
CPU Characteristics: 2000
CPU MHz: Integrated
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 512 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: PGI Workstation Complete Version 7.2-1 PathScale Compiler Suite, Release 3.2 Beta
Auto Parallel: Yes
File System: ext3
System State: Multi-User SuSE Run Level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

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Hardware (Continued)

L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (8x2GB PC2-5300P, CL5, dual rank ECC)
Disk Subsystem: 1 x 400 GB SATA II
Other Hardware: None

Software (Continued)

Other Software: binutils 2.18.50

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	276	49.3	276	49.2	278	48.9	251	54.1	249	54.6	253	53.8
416.gamess	1625	12.0	1623	12.1	1623	12.1	1529	12.8	1526	12.8	1531	12.8
433.milc	706	13.0	706	13.0	706	13.0	690	13.3	689	13.3	685	13.4
434.zeusmp	781	11.7	776	11.7	775	11.7	781	11.7	776	11.7	775	11.7
435.gromacs	707	10.1	708	10.1	708	10.1	579	12.3	578	12.3	580	12.3
436.cactusADM	124	96.2	124	96.8	130	91.9	124	96.5	119	100	125	95.9
437.leslie3d	760	12.4	761	12.4	759	12.4	668	14.1	679	13.8	742	12.7
444.namd	850	9.44	850	9.44	850	9.44	756	10.6	755	10.6	754	10.6
447.dealII	795	14.4	795	14.4	796	14.4	597	19.2	598	19.1	599	19.1
450.soplex	946	8.81	947	8.81	947	8.81	938	8.89	941	8.86	938	8.89
453.povray	427	12.5	429	12.4	430	12.4	361	14.7	359	14.8	360	14.8
454.calculix	654	12.6	654	12.6	655	12.6	536	15.4	536	15.4	536	15.4
459.GemsFDTD	432	24.5	428	24.8	427	24.9	380	28.0	380	27.9	380	27.9
465.tonto	849	11.6	851	11.6	844	11.7	763	12.9	763	12.9	764	12.9
470.lbm	995	13.8	881	15.6	888	15.5	887	15.5	887	15.5	887	15.5
481.wrf	625	17.9	625	17.9	626	17.9	489	22.9	490	22.8	490	22.8
482.sphinx3	1307	14.9	1310	14.9	1344	14.5	1231	15.8	1171	16.6	1179	16.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

powersave -f is applied to set CPU to maximum frequency prior to run
stacksize is set to unlimited prior to run
ulimit -l 2457600
PGI_HUGE_PAGES set to 150
(Total number of huge pages available is 1200)

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>



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Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Fortran benchmarks:

pgf95

Benchmarks using both Fortran and C:

pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fastsse -Msmartalloc=huge:896 -Mconcur -Mfprelaxed -Mipa=jobs:4
-Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

C++ benchmarks:

-fastsse -Msmartalloc=huge:896 -Mfprelaxed -Mconcur --zc_eh
-Mipa=jobs:4 -Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

Fortran benchmarks:

-fastsse -Mfprelaxed -Msmartalloc=huge:896 -Mconcur -Mipa=jobs:4
-Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-fastsse -Msmartalloc=huge:896 -Mconcur -Mfprelaxed -Mipa=jobs:4
-Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

Peak Compiler Invocation

C benchmarks (except as noted below):

pgcc

470.lbm: pathcc

C++ benchmarks (except as noted below):

pathCC

444.namd: pgcpp

Fortran benchmarks (except as noted below):

pgf95

459.GemsFDTD: pathf95

Benchmarks using both Fortran and C:

pgcc pgf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Peak Optimization Flags

C benchmarks:

433.milc: -fastsse -Msmartalloc=huge:896 -Msafepr -Mconcur
-Mfpelaxed -Mipa=jobs:4 -Mipa=inline -Mipa=arg
-Mipa=const -Mipa=ptr -Mipa=shape -tp barcelona-64
-Bstatic_pgi

470.lbm: -march=barcelona -Ofast -CG:sse_cse_regs=0
-CG:locs_shallow_depth=1 -m3dnow -apo

482.sphinx3: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=jobs:4(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mfpelaxed -Msmartalloc -tp barcelona-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=jobs:4(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Munroll=n:4 -Munroll=m:8 -Msmartalloc=huge:896 -Mnodepch
-Mfpelaxed --zc_eh -tp barcelona-64 -Bstatic_pgi

447.dealII: -march=barcelona -Ofast -static -INLINE:aggressive=on
-fno-exceptions -m32 -apo

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -TENV:frame_pointer=off
-LNO:prefetch=1 -OPT:malloc_alg=1 -CG:load_exe=0 -m32 -apo

453.povray: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo

Fortran benchmarks:

410.bwaves: -fastsse -Msmartalloc -Mprefetch=distance:12 -Mprefetch=nta
-Mconcur -Mloop32 -Mpre -Mfpelaxed -Mipa=jobs:4
-Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

416.gamess: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=jobs:4(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=noaltcode -Mprefetch=t0 -Mfpelaxed -tp barcelona-64
-Bstatic_pgi

434.zeusmp: basepeak = yes

437.leslie3d: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mconcur=noaltcode(pass 2) -Mipa=jobs:4(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=fuse -Msmartalloc=huge:896 -Mprefetch=distance:8
-Mprefetch=t0 -Mfpelaxed -tp barcelona-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -apo

465.tonto: -fastsse -O4 -Mvect=noaltcode -Msmartalloc=huge:896
-Mprefetch=distance:8 -Mprefetch=t0 -Mfpelaxed
-Mipa=jobs:4 -Mipa=fast -Mipa=inline -tp barcelona-64
-Bstatic_pgi

Benchmarks using both Fortran and C:

435.gromacs: -fastsse -Msmartalloc=huge:896 -Mfpelaxed -Mconcur
-Mfpapprox=rsqrt -Mipa=jobs:4 -Mipa=fast -Mipa=inline
-tp barcelona-64 -Bstatic_pgi

436.cactusADM: -fastsse -Msmartalloc=huge:896 -Mfpelaxed -Mconcur -Mdse
-Mipa=jobs:4 -Mipa=fast -Mipa=inline -tp barcelona-64
-Bstatic_pgi

454.calculix: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=jobs:4(pass 2) -Mipa=fast(pass 2) -Mipa=inline(pass 2)
-fastsse -Msmartalloc=huge:896 -Mloop32 -Mprefetch=t0
-Mpre -Mfpelaxed -tp barcelona-64 -Bstatic_pgi

481.wrf: -fastsse -Mvect=noaltcode -Msmartalloc
-Mprefetch=distance:8 -Mconcur=noaltcode -Mfpelaxed
-tp barcelona-64 -Bstatic_pgi

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/fsc-mix-pgi-path.20080709.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/fsc-mix-pgi-path.20080709.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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